



MIT COLLEGE OF RAILWAY ENGINEERING AND RESEARCH

BARSHI-413401

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

AY : 2025-26

Time Table

Class: TY BTECH

Semester: II

Date (W.e.f):27/01/2026

Time & Day	09.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-1.30	1.30 - 2.15	02.15-03.15	03.15-04.15	04.15-05.15
Mon	VLSI (PRB) [A-316]	ESD(PSH) [A-316]	SHORT BREAK	AC&MS (SRR) [A-316]	ES (ASV) [A-316]	LUNCH BREAK	OC (JLM) [A-316]	MDM-IV -T1 AVS [CCLab-A411A] MDM-IV -T2 SRR [µC Lab-A314] MDM-IV -T3 PSH [Lang. LabA117]	
Tue	AMC (MDA) [A-316]	VLSI (PRB) [A-316]		T1-ES [MCA. LAB A-314] T2-VLSI [PR LAB - A-309] T3- ESD [ECAD. LAB A-307]	ES (ASV) [A-316]		VLSI (PRB) [A-316]	T&P	
Wed	ESD(PSH) [A-316]	ES (ASV) [A-316]		AMC (MDA) [A-316]	OC (JLM) [A-316]		AC&MS (SRR) [A-316]	ESD(PSH) [A-316]	GFM
Thur	MDM-IV [A-316]	AMC (MDA) [A-315]		T1, T2,T3 -AC&MS [A-315]			T2- ES [MCA. LAB A-314] T1- ESD [ECAD LAB A-307] T3- VLSI [PR LAB - A-309]		T&P
Fri	MDM-IV [A-316]	AC&MS (SRR) [A-316]		T1- MINI PRO PR LAB - A-309] T2,T3 - MINI PRO PR LAB2 - A-409]			ES (ASV) [A-316]	OC (JLM) [A-316]	T&P
Sat	T1-VLSI (PR LAB - [A-309] T2-ESD PR [ECAD.LABA-307] T3- ES [MCA. LAB A-314]			T1- MINI PRO PR LAB - A-309] T2,T3 - MINI PRO PR LAB2 - A-409]			VLSI (PRB) [A-316]	ES (ASV) [A-316]	GFM

Sr. No	Course	Location	Faculty
1	Advance Mobile Communication	[A-316]	Prof. M.D. Andhare
2	Embedded System	[A-316]	Prof. A.S. Velapure
3	Electronic System Design	[A-316]	/Prof. P. S. Hiremath
4	Optical Communication	[A-316]	Prof.Jivan Musale
6	VLSI Design	[A-316]	Prof.Pankaj Bhusari
7	AC&MS in Railway	[A-316]	/Prof.Surekah Raut
8	MDM-IV	[A-316]	/Prof.Alka Taur
9	MINI Project	[A-316]	Prof.S.H.Mali
Class Coordinator:		/ Prof. P. S. Hiremath	


Time Table Coordinator


HOD


Academic Dean


Principal

HoD - Electronics & Telecommunication Engineering
MIT College of Railway Engineering & Research,
Jargaon, Barshi - 413401.